

The logo for embit, featuring the word "embit" in a lowercase, sans-serif font. To the right of the text is a stylized graphic consisting of three concentric, curved lines that resemble a signal or a stylized 'e'. The logo is positioned on a horizontal green bar that spans the width of the page.

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# Important Procedure For Re- programming The EMB- LR1276S

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## Application Note

**embit s.r.l.**

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## Document information

### Versions & Revisions

Revision	Date	Author	Comments
1.0	2019-09-16	M. De Donno	Revision 1

### References

Ref	Version	Date	Author	Title
[1]	Rev. 1.5	2015	Embit	Embit Binary Interface

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## 1 Introduction

The EMB-LR1276S IEEE mac address is saved in its flash memory.

This application note will show you how to avoid to overwrite (and lose) the IEEE mac address of the module if you are going to re-program it.

## 2 Procedure for keeping the IEEE mac address

There are two ways to avoid to lose the EMB-LR1276S IEEE mac address, they are explained in the next paragraphs.

### 2.1 Read and Keep

If you are going to re-program the EMB-LR1276S or you are going to do the Update Over The Air, before doing it you need to follow these steps:

1. Send EBI service command “Device IEEE mac address” (0x7E 0x20) [1]. The module will reply with its own mac address (8 byte).
2. Write it somewhere.
3. Re-program the EMB-LR1276S (or start the OTA Update) with the new EBI firmware.
4. Send the command “Device IEEE mac address” (0x7E 0x20) followed by the 8 byte of the mac address saved in step 2.

### 2.2 Re-program using Atmel Studio

If you are going to re-program the EMB-LR1276S with your own firmware using Atmel Studio you need to follow these steps to avoid to lose the IEEE mac address:

1. Open Atmel Studio.
2. Open the project’s properties.
3. Open the “Tool” window.
4. In the drop-down menu of “Programming Settings” select “Erase only program area” (Figure 1).

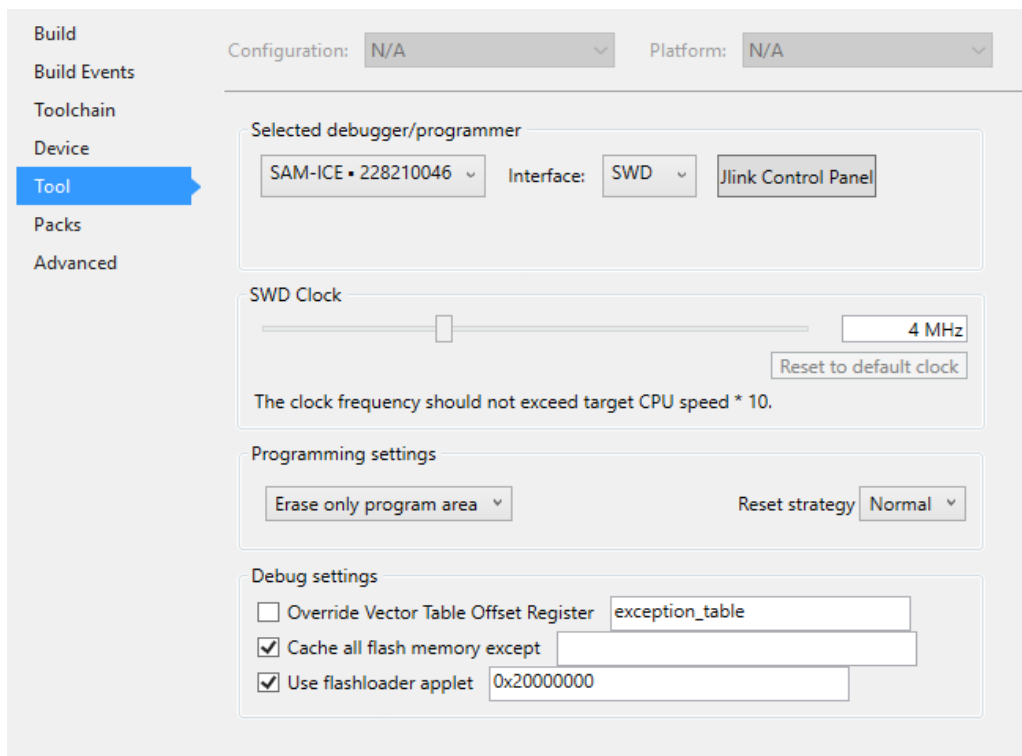


Figure 1.

5. From “Debug” menu click on “Start Without Debugging”.

**IMPORTANT: DO NOT ERASE the flash memory before programming.**

## **3 Annex**

### **3.1 Disclaimer**

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